

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Timothy J. Williams

Assignee:

Cypress Semiconductor Corporation

Title:

CIRCUIT AND METHOD FOR PROVIDING A PRECISE CLOCK FOR

DATA COMMUNICATIONS

Serial No.:

09/668,801

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Examiner:

Cao, C.

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

DECLARATION OF TIMOTHY J. WILLIAMS PURSUANT TO 37 C.F.R. S 1.132

- I, Timothy J. Williams, hereby declare as follows:
- I am presently employed as a Principal Design Engineer by Cypress Semiconductor Corporation.
- 2 I have been employed by Cypress Semiconductor Corporation since March 1996 in various capacities.
- 3. I have reviewed the relevant portions of the specification and claims of the present invention and U.S. Patent No. 6,407,641 to Williams et al. (hereinafter "the '641 Patent").

4. I understand that in one embodiment, the present invention concerns:

An apparatus comprising a circuit configured to (i) generate an output having a frequency and (ii) adjust said frequency in response to a measured duration of a known time interval associated with a predefined bit pattern occurring in an input data stream.

5. I understand that in another embodiment, the present invention concerns:

An apparatus comprising (i) means for generating an output having a frequency, (ii) means for measuring a known time interval between occurrences of a predefined bit pattern in an input using the output and (iii) means for adjusting the generating means in response to the measurement.

6. I understand that in yet another embodiment, the present invention concerns:

A method for providing a precise clock using a precisely known time interval having a known precise duration of a data stream comprising the steps of (A) measuring the known time interval between occurrences of a predefined bit pattern in the data stream using the clock and (B) adjusting the clock in response to a difference between the measurement and the known duration.

7. I am one of the named inventors of the '641 Patent.

- The subject matter disclosed and not claimed in the '641 Patent and relied upon in the 8. rejections in the Office Action mailed October 13, 2004 was conceived by me or derived from my own work.
- 9. I hereby declare that all statements made herein of my knowledge are true and that all statements made on information and belief are true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or patents issued therefrom.

Date: Dec 6, 2004